|  |  |  |  |
| --- | --- | --- | --- |
| **logoUniversity of Management & Technology**  School of Science & Technology  Department of Electrical Engineering | | | |
| EL-447 Power Electronics Lab | | | |
| **Lab Schedule** | Mon: 10:40-01:20 (Section C)  Tue: 10:40-13:20 (Section D)  Wed: 10:40-13:20 (Section C1)  Thu: 10:40-13:20 (Section D1) | **Semester** | Fall 2012 |
| **Pre-requisite** | EE\_111: Circuits Analysis  EE\_209: Electronic Devices and Circuits | **Credit Hours** | 2 |
| **Instructor(s)** | Rukhsar Ahmad(Sec D)  Tabraiz Ahmed Alvi(Sec C)  Nauman Ahmad(Sec C1)  Imran Ali(Sec D1) | **Contact** | [rukhsar.ahmad@umt.edu.pk](mailto:rukhsar.ahmad@umt.edu.pk)  [tabraiz.alvi@umt.edu.pk](mailto:tabraiz.alvi@umt.edu.pk)  [nauman.ahmad@umt.edu.pk](mailto:nauman.ahmad@umt.edu.pk)  [imran.ali@umt.edu.pk](mailto:imran.ali@umt.edu.pk) |
| **Office** | SST Campus | **Office Hours** | See office window |
| **Teaching Assistant** | None | **Contact** | NA |
| **Lab Work Objectives** | |  | | --- | | This course provides an introduction to power electronics and its applications. The course starts with coverage of modern power semiconductor devices, their characteristic, and drive circuit design. Topologies of power electronic circuits including single phase and three phase ac-dc rectifiers, ac-ac voltage controllers, dc-dc converters, and dc-ac inverters will be covered along with their control techniques. Applications of modern power electronic devices are also included with emphasis on switch mode and linear regulators. These objectives confirm to the ones’ listed in HEC guidelines as a, d, e, & f. | | | |
| **Expected Outcomes** | In accordance with HEC curriculum outcomes a, d, & k, students at the end of the course should be able to:   * Develop and quantify common performance objectives for power electronic circuits. * Develop simple power electronic converter topologies to meet certain functional specifications. * Analyze power electronic converter operation to develop design guidelines for choice of switching devices and reactive elements. | | |
| **Grading Policy** | Final Viva & Performance: 40% Lab Manuals:40%  Project :20% | | |

**Lab Schedule**

|  |  |  |
| --- | --- | --- |
| **Exp #** | **Experiment Name** | **Study Material** |
| 1. | Oscilloscope and Function Generator Operation | Lab Manual |
| 2. | Single phase half wave uncontrolled rectifier with R,RL load | Lab Manual |
| 3. | Single phase full wave uncontrolled rectifier with R,RL load | Lab Manual |
| 4. | VI Characteristics of Thyristor | Lab Manual |
| 5. | SCR using R firing circuit | Lab Manual |
| 6. | SCR using RC firing circuit | Lab Manual |
| 7. | SCR power control using UJT phase control | Lab Manual |
| **Mid Term Viva & Performance** | | |
| 8. | Single phase half wave controlled rectifier with R,RL load | Lab Manual |
| 9. | Single phase full wave controlled rectifier with R,RL load | Lab Manual |
| 10. | A simple series voltage regulator | Lab Manual |
| 11. | A simple shunt voltage regulator | Lab Manual |
| 12. | An improved variable output series voltage regulator | Lab Manual |
| 13. | A variable voltage regulator using op-amp with over current protection | Lab Manual |
| 14. | Design and simulation of Buck regulator and Boost regulator | Text Book |
| 15. | Design and simulation of Buck-Boost regulator | Text Book |
| **Final Viva & Performance** | | |